

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Blackshear et al.

Serial No.: 10/056,256

Group Art Unit: 2811

Filed: January 24, 2002

Examiner: Nguyen, Cuong Quang

For: VERTICALLY STACKED MEMORY CHIPS IN FBGA PACKAGES

Honorable Assistant Commissioner of Patents
Washington, D.C. 20231**RESPONSE TO RESTRICTION REQUIREMENT**

Sir:

In response to the Office Action dated August 27, 2002, please consider the following:

FAX COPY RECEIVED

SEP 13 2002

TECHNOLOGY CENTER 2800

REMARKS

In response to the Restriction Requirement, Applicant hereby elects the invention of Group I, (Claims 1-14), without traverse. Applicant reserves the opportunity to file a Divisional Application for the non-elected invention later. Early, favorable prosecution on the merits is respectfully requested.

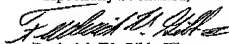
Should the Examiner find the application to be other than in condition for allowance, the Examiner is requested to contact the undersigned at the local telephone number listed below to discuss any other changes deemed necessary in a telephonic or personal interview.

10/056,256
FIS920000273US1

2

A conditional petition is made for any extension of time which may become necessary. The Commissioner is authorized to charge any fees for such extension and to credit any overpayment in fees to Attorney's Deposit Account No. 09-0458.

Respectfully Submitted,



Frederick W. Gibb, III

Reg. No. 37,629

Date: 9/13/02
McGinn & Gibb, PLLC
2568-A Riva Road, Suite 304
Annapolis, Maryland 21401
(301) 261-8071
Customer No. 29154

FAX COPY RECEIVED

SEP 13 2002

TECHNOLOGY CENTER 2800